

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
Toshihisa Nagata et al.

Application No.: 10/577,666

Confirmation No.: 8451

Filed: May 1, 2006

Art Unit: 2838

For: SEMICONDUCTOR DEVICE AND VOLTAGE  
REGULATOR, WITH FORCE BALANCED  
MONITOR TRANSISTOR CONTAINING  
PLURAL TRANSISTORS (As Amended)

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Examiner: Harry Raymond Behm

**AMENDMENT AFTER FINAL**

Mail Stop AF  
Commissioner for Patents  
U.S. Patent and Trademark Office  
Alexandria, VA 22314

Dear Madam:

**INTRODUCTORY COMMENTS**

Responsive to the Office Action dated February 12, 2009, please amend the captioned application as follows:

**Amendments to the Specification** appear on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims that begins on page 3 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.